



COLLEGE OF ENGINEERING & TECHNOLOGY

Department: Electronics and Communications Engineering, Cairo

Graduation Project Description Form

Project Title: Microwave image reject mixer

Duration from mo/year _____ till mo/year _____

Project Supervisor(s): Dr. Atef Ghuniem

Product Category

Algorithm _____ Hardware _____ Software _____

Standards:

Safety: UL, CE _____ IEEE _____ FCC _____ Other _____

Practical Realization Form

PCB _____ Firmware _____ Embedded CPU Kit (ARM, ..etc): _____

PC Software _____ Ready-made Package _____ DSP Kit _____ FPGA Kit _____

VLSI Schematics _____ VLSI Layout _____ VLSI Silicon (ASIC) _____

Language

VHDL/Verilog _____ Matlab _____ C/C++/Java _____

Productization

Finished Product Form: _____ Possible Commercialization _____

Amount of funds needed for buying components: _____

IEEE GOLD Made-In-Egypt/Engineering Day: _____

ITAC (ITIDA) or NTRA Funding Application: _____

Testing

Functional _____ Simulation _____ Parameters _____ Final Hardware _____ Other: _____

Lab Test Setup

EMC _____ Environmental _____ Microwave _____ Analog Lab _____ Other: _____

CAD Tools (*No unauthentic software is allowed*):

Elective Classes Required:



COLLEGE OF ENGINEERING & TECHNOLOGY

Department: Electronics and Communications Engineering, Cairo

Graduation Project Description Form

Abstract

Reviewing the different techniques for image frequency rejection.
Design an image reject mixer circuit.
Performing computer simulation using ADS.
Circuit realization and measurements.

References and Links