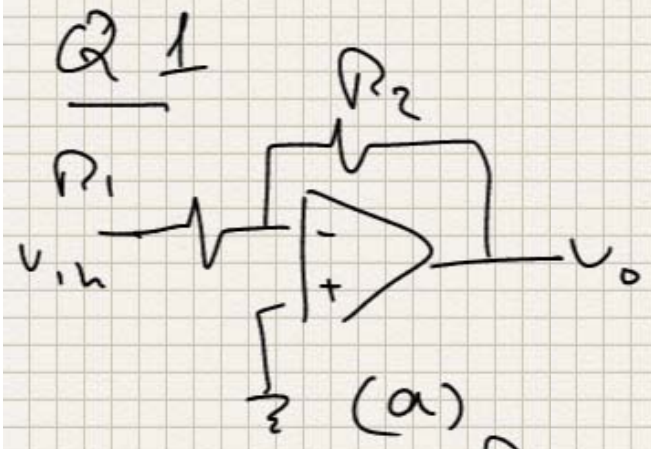
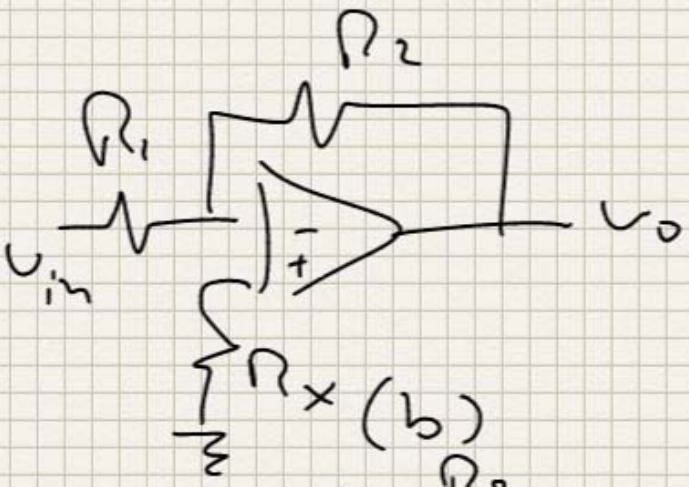
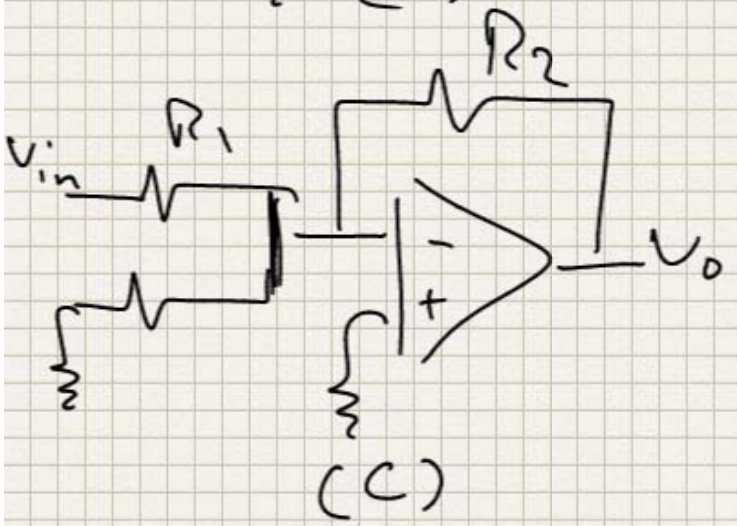
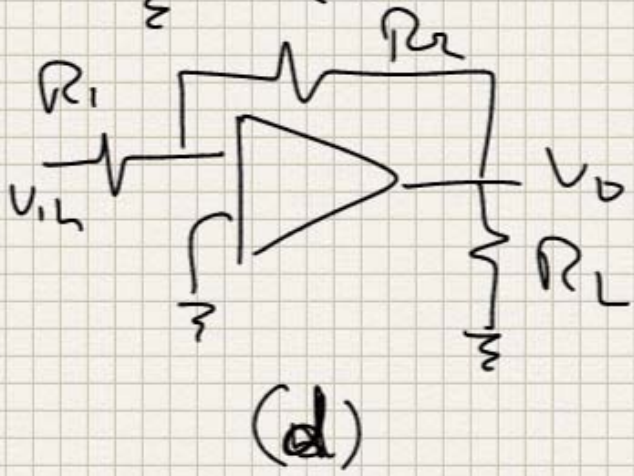
Q 1









for figure a, b, C & d

find the closed loop gain.

$$\frac{V_o}{V_{in}}$$

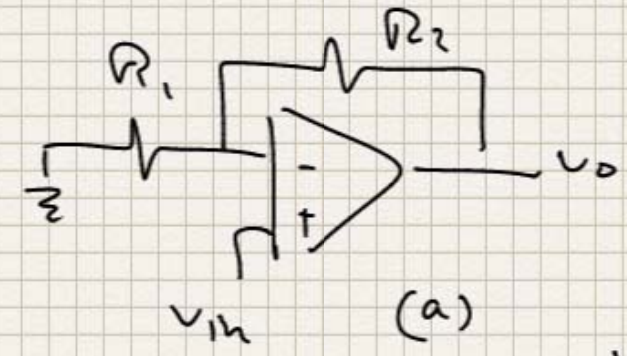
Q2 (2) Design Inverting Amplifier

Closed loop gain = -100.

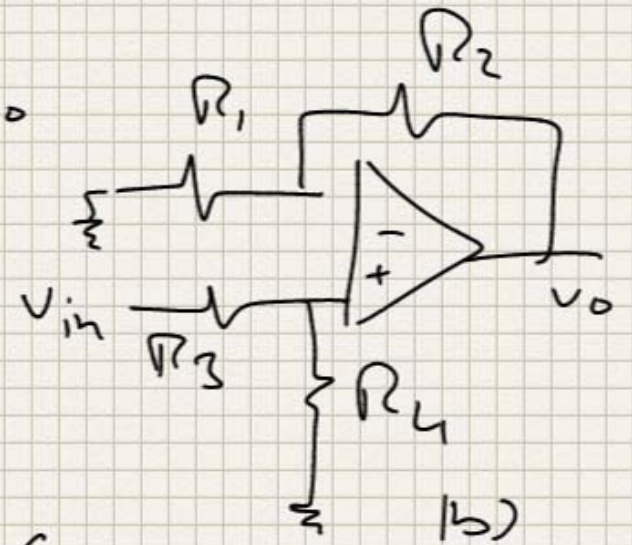
a) Find R_1 & R_2

b) What is the max V_{in} if $V_{sat} = \pm 10V$

Q3



(a)

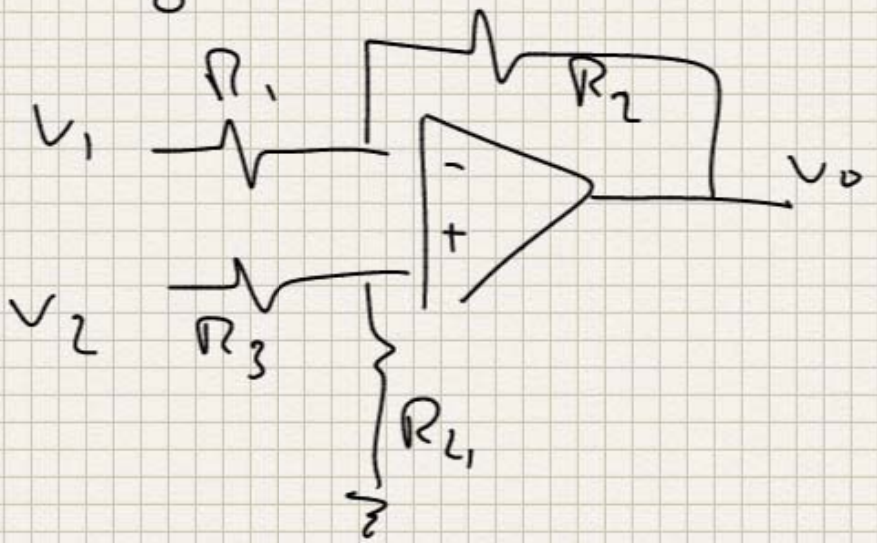


(b)

for (a) & (b)

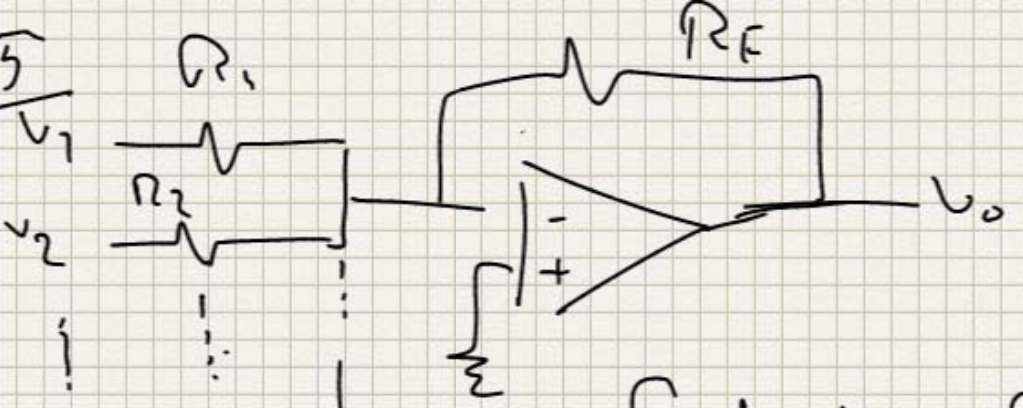
find the Closed Loop Gain.

Q4) for this figure find v_o f(v_1, v_2) using superposition



b) if $R_1 = R_3$, $R_2 = R_4$ find $\left(\frac{v_o}{v_2 - v_1}\right)$

Q5



find V_o $f_n(V_1, V_2 \ldots V_N)$

Q6 Design Using any number of opAmps & Resistors R

$$V_o = -(V_1 + 2V_2) + V_3$$

Q7 Design an amplifier With Gain 11 Using opAmps
